

DRAWING CORRECTIONS

Figs. 2, 3, and 5 are resubmitted herewith and have been amended to show the legend 'PRIOR ART' as requested.

REMARKS

Reconsideration of the subject application as amended herein is respectfully requested.

The Examiner has indicated that dependent claims 2-10, 12- 16, 18-22 and 24-28 are allowed. Accordingly, independent claims 1, 11, 17 and 23 have been amended to include limitations of claims 2, 12, 18 and 24 respectively. In addition new 29 includes the limitations of claims 1 and 8, new claim 30 includes the limitations of claims 11 and 13, new claim 31 includes the limitations of claims 17 and 19 and new claim 32 includes the limitations of claims 23 and 25.

The Applicants would like to emphasize that an important feature of the invention is that it uses two well-known tools of the trade, CCI bits and RAM in a completely novel way. Most other schemes uses either one or the other of these tools for various purposes. As discussed at length in the specification, Ogino uses these two tools as means of providing redundancy. That is, if one is missing then the other is used. However, as explained in the specification, and described in the claims, that is not what is done herein. In this application, a scheme is used to overcome the problem of a CCI bits being stripped away, using a two step process. First, a check is performed to determine if a RAM is present. If it, and the CCI bits are missing then a default setting is used. Thus the RAM itself does not determine how the signal is processed. The RAM is used merely to indicate whether CCI bits should be present or not. There is nothing in the prior art to indicate this kind of operation.

It is respectfully submitted that the subject application is now in condition
for allowance.

Respectfully submitted,

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